CTL Model Checking

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Verifying cyberphysical systems

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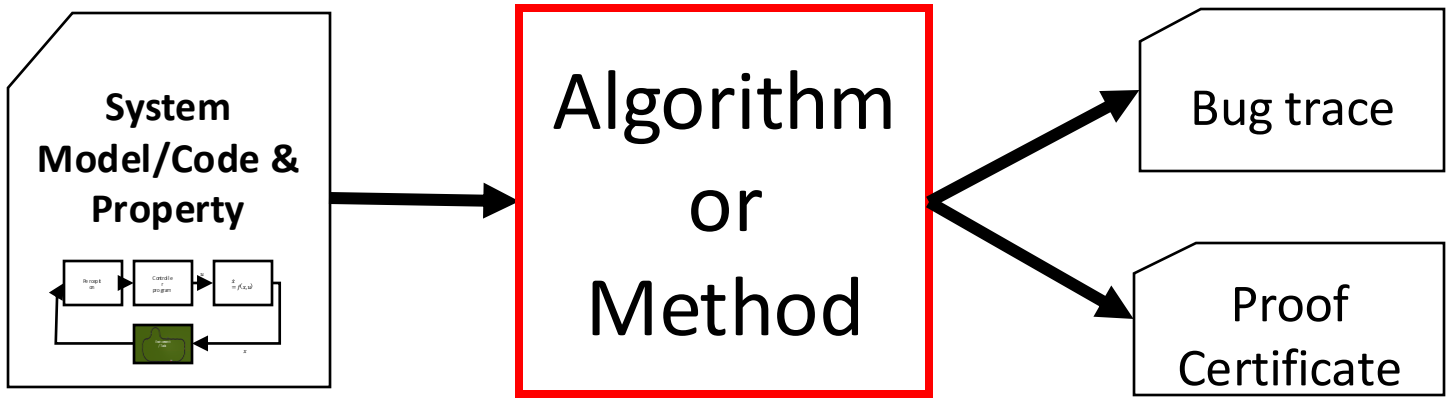
This lecture: Requirements

System. A program/system for *lane keeping control* for vehicles

Model/assumptions: automaton, ODEs, hybrid automata

Requirement. The vehicle does not go outside the lane boundaries



Verification tool

counterexample. A particular environment situation (lane geometry, sensor failure, computer configuration) that makes the vehicle go outside lanes

A mathematical proof that establishes that for all *allowed* inputs and environments the vehicle stays with the lane

When can we build such a tool? How expensive is it? How well is it going to work? Under what assumptions?

Requirements and safety in the real world

Requirements analysis: Set of tasks that ultimately lead to the determination and documentation of the design requirements that the product must meet:

E.g. “0 to 60 mph in 4 seconds on flat road”,

“Petrol car can emit no more than 60mg/km” EURO 6.

Safety standards: Provide *guidelines* and *processes* for developing safety-critical systems.

E.g. DO-178C standard is enforced by the FAA for certifying aviation software

ISO2626 is used for functional safety of cars

Standards for Advanced Autonomous and AI-enabled systems are being developed

Requirements thus far: Invariants and stability

Models automaton, hybrid automaton $\mathcal{A} = \langle X, \Theta, A, \boldsymbol{\mathcal{D}}, \boldsymbol{T} \rangle$

Requirements: $I \subseteq val(X)$, such that $Reach_{\mathcal{A}}(\Theta) \subseteq I$

Given an **unsafe set** $U \subseteq val(X)$ we can check whether $I \cap U = \emptyset$ to infer that $Reach_{\mathcal{A}}(\Theta) \cap U = \emptyset$

Asymptotic stability: Does $\alpha(\mathrm{x}_0, \mathrm{t}) \to 0$ as $t \to 0$.

What about more general types of requirements, e.g.,

“Eventually the light turns red and prior to that the orange light blinks”

“After failures, eventually there is just one token in the system”

How to express and verify such properties?

Outline

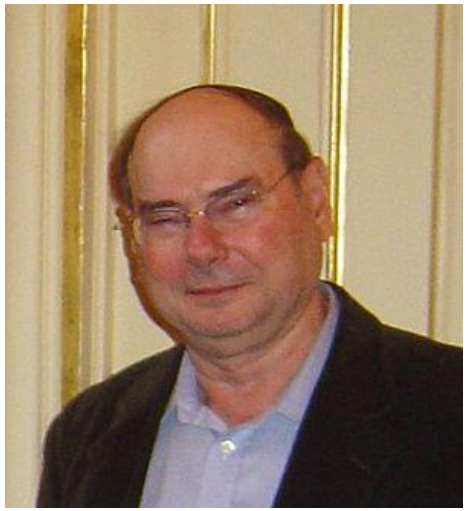
- Temporal logics
  - Computational Tree Logic (CTL)
- CTL model checking for automata
  - Setup
  - CTL syntax and semantics
  - Model checking algorithms
  - Example

- References: Model Checking, Second Edition, by Edmund M. Clarke, Jr., Orna Grumberg, Daniel Kroening, Doron Peled and Helmut Veith

- Principles of Model Checking, by Christel Baier and Joost-Pieter Katoen

Introduction to temporal logics

Temporal logics: Formal language for representing, and reasoning about, propositions qualified in terms of a sequence



Amir Pnueli received the ACM Turing Award (1996) for seminal work introducing **temporal logic** into computer science and for outstanding contributions to program verification.

Large follow-up literature, e.g., different temporal logics MTL, MITL, PCTL, ACTL, STL, applications in synthesis and monitoring

Setup: States are labeled

We have a set of atomic propositions (AP)

These are the properties that hold in each state, e.g., “light is green”, “has 2 tokens”

We have a *labeling function* that assigns to each state, a set of propositions that hold at that state

$L: Q \rightarrow 2^{AP}$

Notations

Automata with state labels but no action labels

$\mathcal{A} = \langle Q, Q_0, D, L \rangle$ with transitions $D \subseteq Q \times Q$

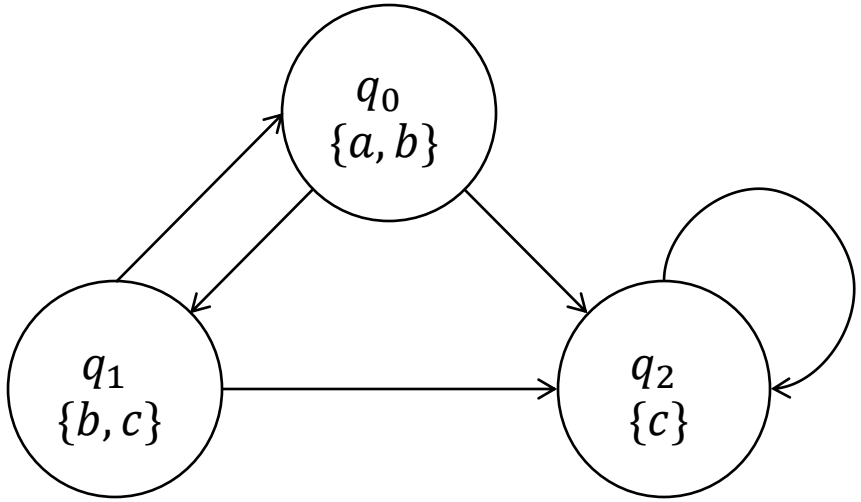
Executions (have no actions) $\alpha = q_0\ q_1 \ldots q_k = \alpha.lstate$

$\alpha[i] = q_i$

$Exec_{\mathcal{A}}$ set of all executions

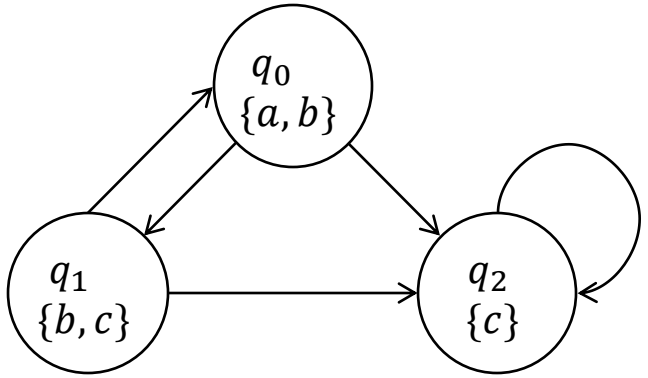
$AP = \{a, b, c\}$

$L(q_0) = \{a, b\}$

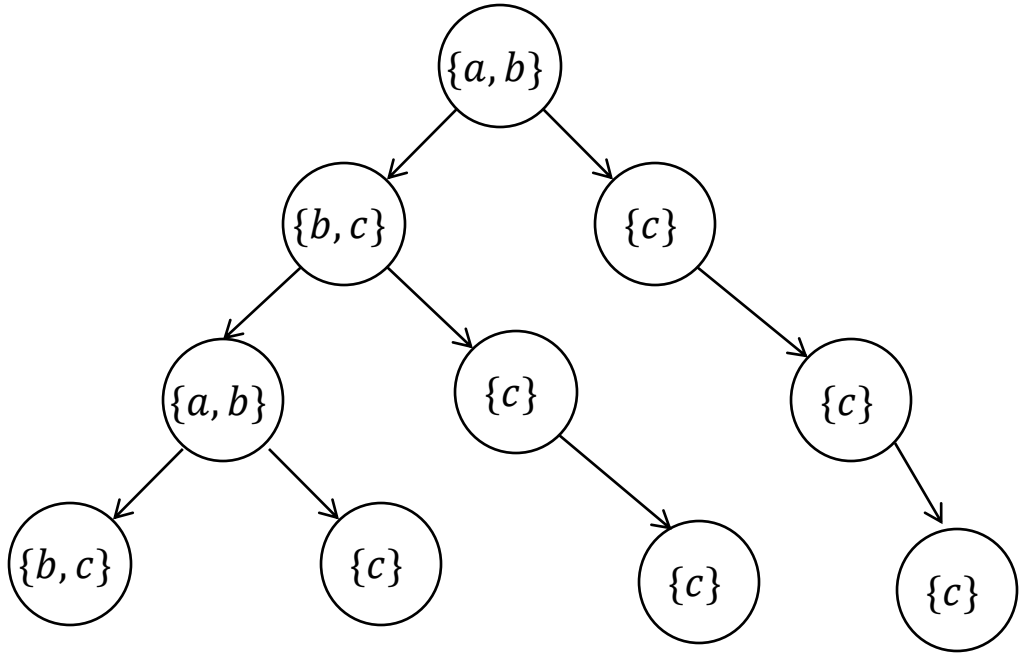


Computational tree logic (CTL)

Unfolding the automaton



We get a tree



A CTL formula allows us to specify subsets of paths in this tree

CTL quantifiers

Path quantifiers

E: Exists some path

A: All paths

Temporal operators

X: Next state

U: Until

F: Eventually

G: Globally (Always)

CTL syntax

CTL syntax: use alternating SF and PF

$State\ Formula\ (SF) ::= true \mid p \mid \neg f_1 \mid f_1 \wedge f_2 \mid E\ \phi \mid A\ \phi$

$Path\ Formula\ (PF) ::= X f_1 \mid f_1 U\ f_2 \mid G f_1 \mid F\ f_1$

where $p \in AP, f_1, f_2 \in SF, \phi \in PF$

Depth of formula: number of production rules used

ϕ: "no collision"
Invariance: $AG\phi$
ϕ: "one token"
Stabilization: $AF\phi$

Examples (depth)

$\mathrm{E}X\ a$; $\mathrm{AXE}X\ a$; $\mathrm{AXE}Xa$ U b; AG AF green; AF AG single token

Depth 3, 5, …

Non-examples

$\mathrm{AX}X\ a$; path and state operators must alternate in CTL

CTL semantics

Automaton $\mathcal{A} = \langle Q, \mathrm{Q}_0, T, L \rangle, q \in Q$ and a CTL formula $\phi, q \vDash \phi$ denotes that q *satisfies* ϕ; $\alpha \vDash \phi$ denotes that path (execution) α satisfies ϕ; $\vDash$ is defined inductively as:

$\mathcal{A}, q \vDash p \quad \Leftrightarrow p \in L(q)$ for $p \in AP$

$\mathcal{A}, q \vDash \neg f_1 \quad \Leftrightarrow \mathcal{A}, q \vDash f_1$

$\mathcal{A}, q \vDash f_1 \wedge f_2 \quad \Leftrightarrow \mathcal{A}, q \vDash f_1 \wedge \mathcal{A}, q \vDash f_2$

$\mathcal{A}, q \vDash E\phi \quad \Leftrightarrow \exists\, \alpha, \alpha.fstate = q, \mathcal{A}, \alpha \vDash \phi$

$\mathcal{A}, q \vDash A\phi \quad \Leftrightarrow \forall\, \alpha, \alpha.fstate = q, \mathcal{A}, \alpha \vDash \phi$

$\mathcal{A}, q \vDash Xf \quad \Leftrightarrow \mathcal{A}, \alpha[1] \vDash f$

$\mathcal{A}, \alpha \vDash f_1\ U\ f_2 \quad \Leftrightarrow \exists i \geq 0, \mathcal{A}, \alpha[i] \vDash f_2\ and\ \forall j < i\ \alpha[j] \vDash f_1$

$\mathcal{A}, \alpha \vDash F\ f_1 \quad \Leftrightarrow \exists i \geq 0, \mathcal{A}, \alpha[i] \vDash f_1$

$\mathcal{A}, \alpha \vDash G\ f_1 \quad \Leftrightarrow \forall i \geq 0, \mathcal{A}, \alpha[i] \vDash f_1$

Automaton satisfies property:
$\mathcal{A} \vDash f$ iff $\forall q \in Q_0, \mathcal{A}, q \vDash f$

Universal CTL operators

$\boldsymbol{X}, \boldsymbol{U}, \boldsymbol{G}$ can be used to derive other operators

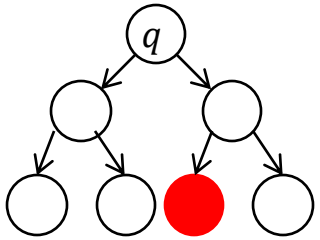
$true\ U\ f \equiv F\ f$

$Gf \equiv \neg F(\neg f)$

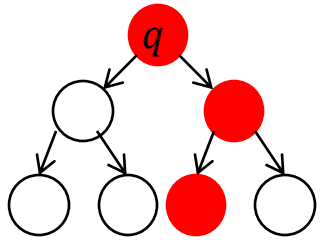
All ten combinations can be expressed using $\boldsymbol{EX}, \boldsymbol{EU}, \boldsymbol{EG}$

AXf	AGf	AFf	AUf	ARf
$\neg EX(\neg f)$	$\neg EF(\neg f)$	$\neg EG(\neg f)$		
EX	EG	EF	EU	ER
EX	EG	$E(true\ U\ f)$	EU	

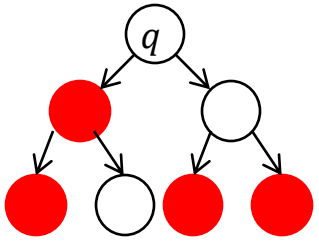
Visualizing semantics



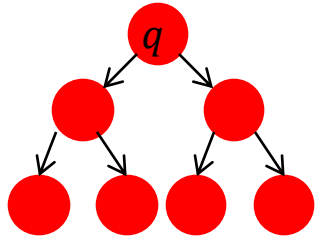
$q \models EF\ red$



$q \models EG\ red$



$q \models AF\ red$



$q \models AG\ red$

CTL Model checking

How to check $\mathcal{A} \vDash f$?

Algorithm for deciding $\mathcal{A} \models f$

Algorithm works by structural induction on the depth of the formula

Explicit state model checking

Compute the subset $Q' \subseteq Q$ such that $\forall q \in Q'$ *we have* $\mathcal{A}, \mathrm{q} \models f$

If $Q_0 \subseteq Q'$ then we can conclude $\mathcal{A} \models f$

Induction on depth of formula

Algorithm computes a function $label: Q \rightarrow CTL(AP)$ that labels each state with a CTL formula

- Initially, $label(q) = L(q)$ for each $q \in \mathrm{Q}$
- At i^{th} iteration $label(q)$ contains all sub-formulas of f of depth $(i-1)$ that q satisfies

At termination $f \in label(q) \Leftrightarrow \mathcal{A}, q \vDash f$

Structural induction on formula

Six cases to consider based on structure of f

$f = p$, for some $p \in AP$,	$\forall q, label(q) \coloneqq label(q) \cup f$
$f = \neg f_1$	if $f_1 \notin label(q)$ then $label(q) \coloneqq label(q) \cup f$
$f = f_1 \wedge f_2$	if $f_1, f_2 \in label(q)$ then $label(q) \coloneqq label(q) \cup f$
$f = EXf_1$	if $\exists q' \in$ Q such that $(q, q') \in T$ and $f_1 \in label(q')$ then $label(q) \coloneqq label(q) \cup f$

$f = E[f_1 U f_2]$ $CheckEU(f_1, f_2, Q, D, L)$ [next slide]
$f = EGf_1$ $CheckEG(f_1, Q, D, L)$ [next slide]

$CheckEU(f_1, f_2, Q, D, L)$

Let $S = \{q \in Q \mid f_2 \in label(q)\}$

for each $q \in S$

  $label(q) \coloneqq label(q) \cup \{E[f_1 U f_2]\}$

while $S \neq \emptyset$

  for each $q' \in S$

    $S \coloneqq S \setminus \{q'\}$

    for each $q \in D^{-1}(q')$ // prestate of q'

      if $f_1 \in label(q)$ then

        $label(q) \coloneqq label(q) \cup \{E[f_1 U f_2]\}$

          $S \coloneqq S \cup \{q\}$

Proposition. For any state $label(q) \ni E[f_1 U f_2]$ iff $q \vDash E[f_1 U f_2]$.

Proposition. Finite Q therefore terminates and in $O(|Q| + |D|)$ steps.

$CheckEG(f_1, Q, D, L)$

From $\mathcal{A}$ we construct a new automaton $\mathcal{A}' = \langle Q', T', L' \rangle$ such that

$Q' = \{q \in Q \mid f_1 \in label(q)\}$

$D' = \{\langle q_1, q_2 \rangle \in D \mid q_1 \in Q'\} = D \; restricted \; to \;\; Q'$

$L' : Q' \to 2^{AP} \;\; \forall \, q' \in Q', L'(q') := L(q')$

Claim. $\mathcal{A}, \mathrm{q} \vDash EG f_1$ iff

(1) $q \in Q'$

(2) $\exists \alpha \in Execs_{\mathcal{A}}$, with $\alpha.fstate = q$ and $\alpha.lstate$ is in a nontrivial ***Strongly Connected Components*** C of the graph $\langle Q', D' \rangle$

Claim. $\mathcal{A}, \mathrm{q} \vDash EGf_1$ iff

(1) $q \in Q'$ and

(2) $\exists \alpha \in Execs_{\mathcal{A}}$, with $\alpha.fstate = q$ and $\alpha.lstate$ is in a nontrivial SCC C of the graph $\langle Q', D' \rangle$

Proof. Suppose $\mathcal{A}, \mathrm{q} \vDash EGf_1$

Consider any execution α with $\alpha.fstate = q$. Obviously, $q \vDash f_1$ and so, $q \in Q'$.

Since Q is finite α can be written as $\alpha = \alpha_0\alpha_1$ where α_0 is finite and every state in α_1 repeats infinitely many times.

Let C be the states in α_1. $C \in Q'$.

Consider any two q_1 and q_2 states in C, we observe that $q_1 \rightleftarrows q_2$, and therefore C is a SCC.

Consider (1) and (2). We construct a path $\alpha = \alpha_0\alpha_1$ such that $\alpha_0.fstate = q$ and $\alpha_0 \in Q'$ and α_1 visits some states infinitely often.

$CheckEG(f_1, Q, D, L)$

Let $Q' = \{q \in Q \mid f_1 \in label(q)\}$

Let $\mathbb{C}$ be the set of nontrivial SCCs of $\langle Q', D' \rangle$

$\boldsymbol{T} = \cup_{C \in \mathbb{C}} \{q \mid q \in C\}$

for each $q \in \boldsymbol{T}$

  $label(q) \coloneqq label(q) \cup \{EGf_1\}$

while $\boldsymbol{T} \neq \emptyset$

  for each $q' \in \boldsymbol{T}$

    $\boldsymbol{T} \coloneqq \boldsymbol{T} \setminus \{q'\}$

    for each $q' \in Q'$ such that $(q', q) \in D'$

      if $\mathrm{E}Gf_1 \notin label(q')$ then

        $label(q') \coloneqq label(q') \cup \{EGf_1\}$

          $\boldsymbol{T} \coloneqq \boldsymbol{T} \cup \{q\}$

Proposition. For any state $label(q) \ni EGf_1$ iff $q \vDash EGf_1$.

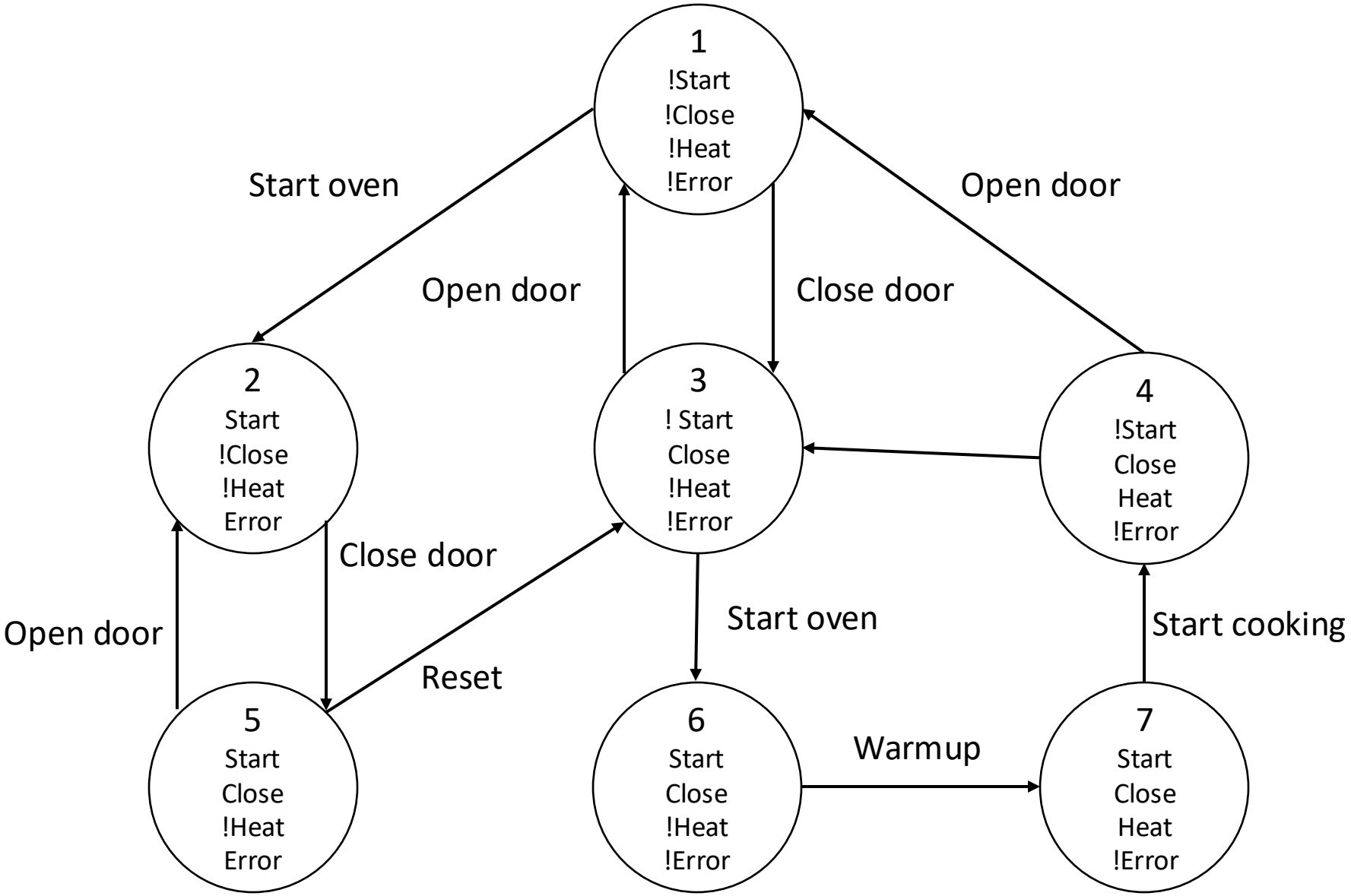
Proposition. Finite Q therefore terminates and in $O(|Q| + |D|)$ steps.

Putting it all together

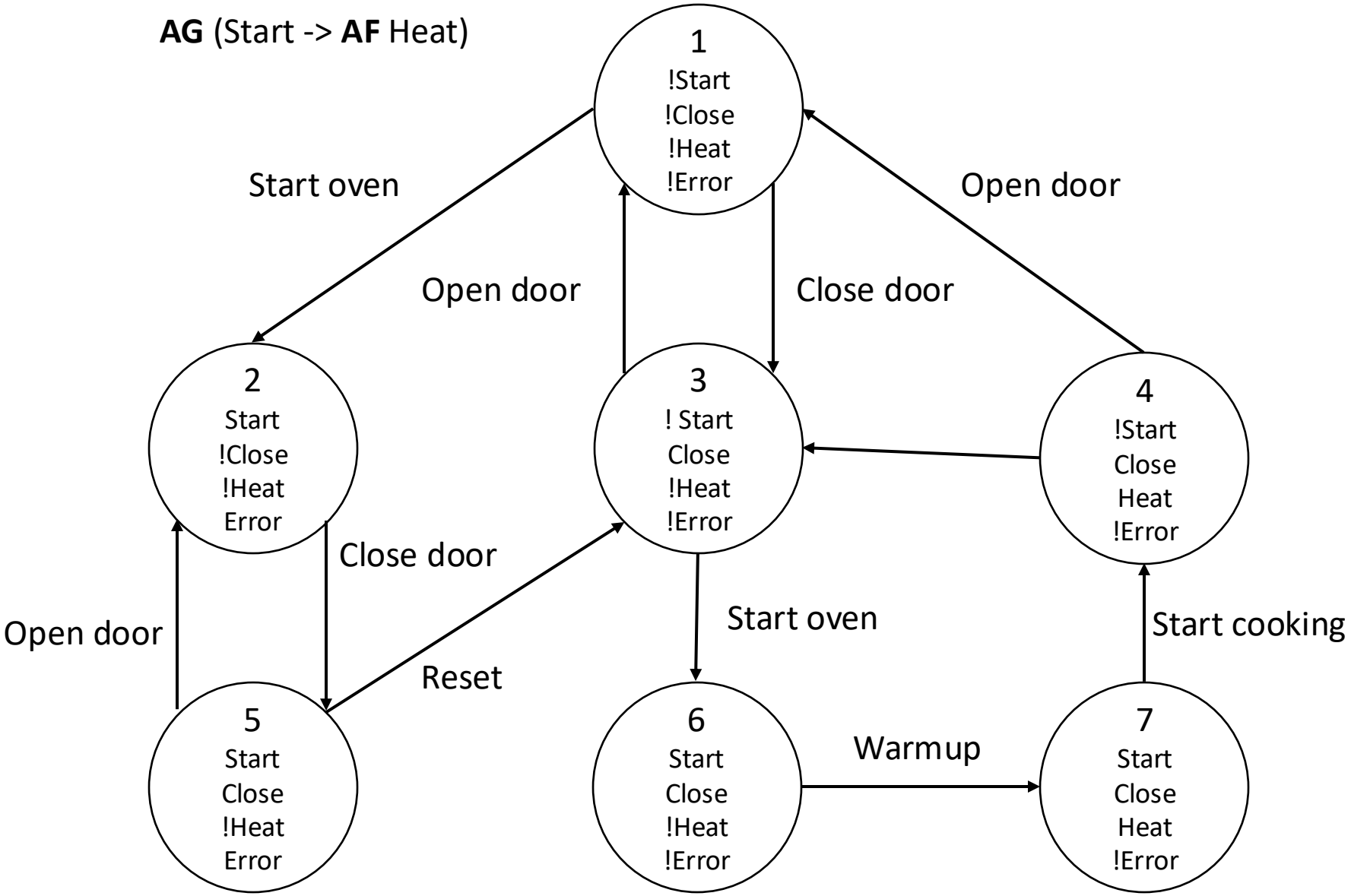
Explicit model checking algorithm input$\mathcal{A} \models f$?
Structural induction over CTL formula

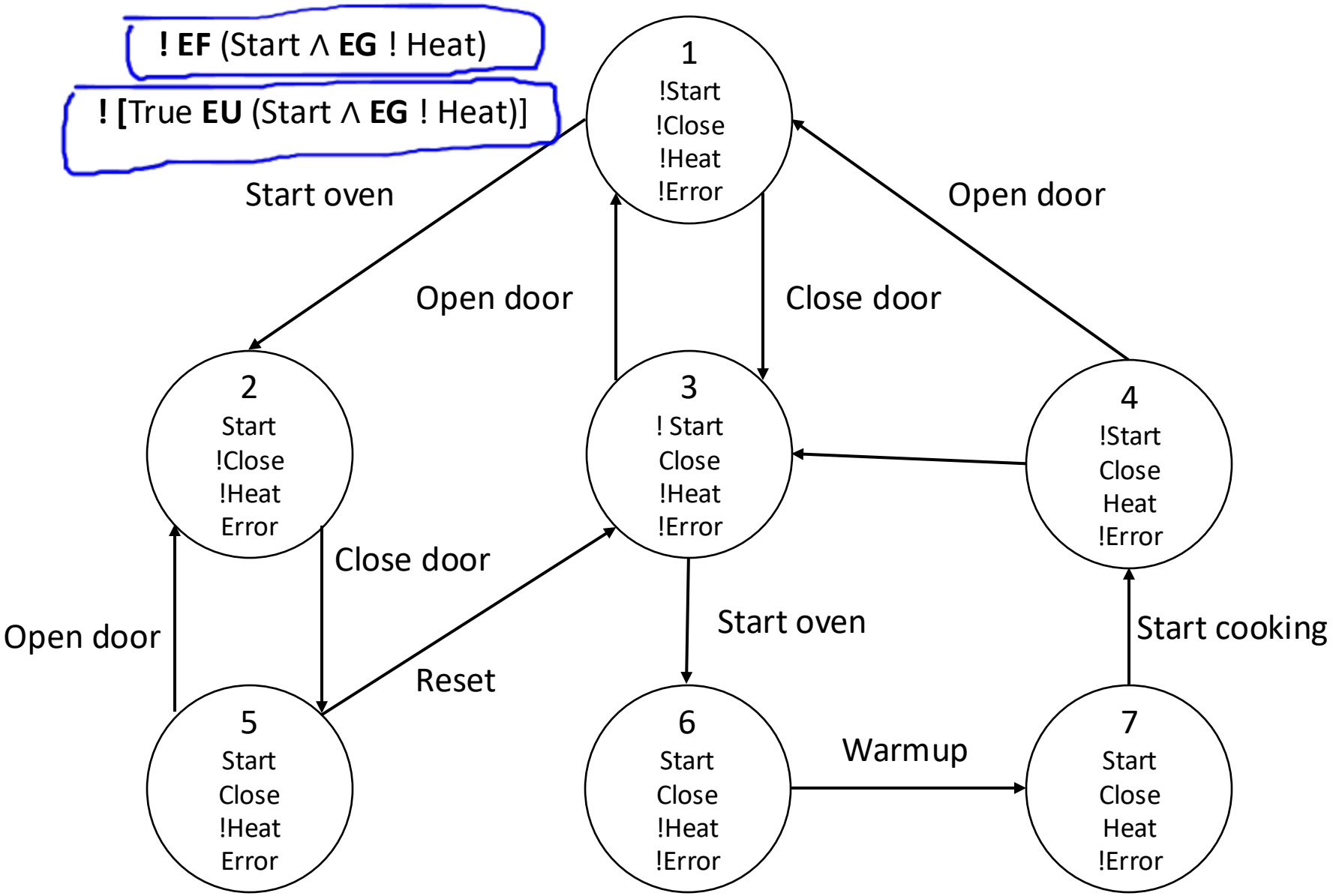
$f = p,$	for some $p \in AP, \forall q, label(q) \coloneqq label(q) \cup \{p\}$
$f = \neg f_1$	if $f_1 \notin label(q)$ then $label(q) \coloneqq label(q) \cup f$
$f = f_1 \wedge f_2$	if $f_1, f_2 \in label(q)$ then $label(q) \coloneqq label(q) \cup f$
$f = EXf_1$	if $\exists q' \in$ Q such that $(q, q') \in T$ and $f_1 \in label(q')$ then $label(q) \coloneqq label(q) \cup f$
$f = E[f_1 U f_2]$	$CheckEU(f_1, f_2, Q, T, L)$
$f = EGf_1$	$CheckEG(f_1, Q, T, L)$

Proposition. Overall complexity of CTL model checkign $O(|f|(|Q| + |D|))$ steps.


1
!Start
!Close
!Heat
!Error
Start oven
Open door
Open door
Close door
2
Start
!Close
!Heat
Error
3
! Start
Close
!Heat
!Error
4
!Start
Close
Heat
!Error
Close door
Open door
Start oven
Start cooking
Reset
5
Start
Close
!Heat
Error
6
Start
Close
!Heat
!Error
Warmup
7
Start
Close
Heat
!Error

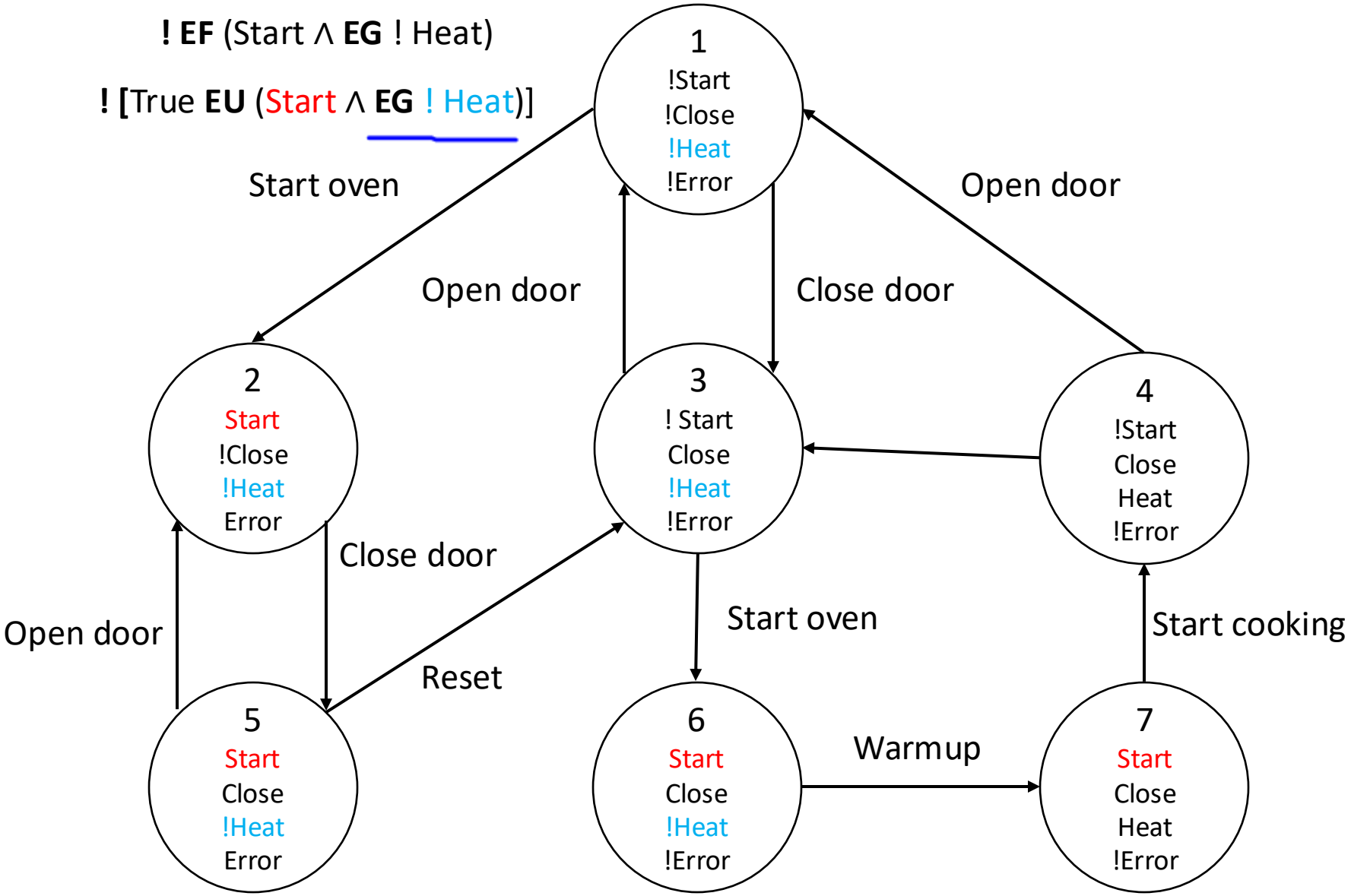
AG (Start -> **AF** Heat)




! EF (Start ∧ EG ! Heat)
! [True EU (Start ∧ EG ! Heat)]
1
!Start
!Close
!Heat
!Error
Start oven
Open door
Open door
Close door
2
Start
!Close
!Heat
Error
3
! Start
Close
!Heat
!Error
4
!Start
Close
Heat
!Error
Close door
Open door
Start oven
Start cooking
Reset
5
Start
Close
!Heat
Error
6
Start
Close
!Heat
!Error
Warmup
7
Start
Close
Heat
!Error

! EF (Start ∧ **EG** ! Heat)

! [True **EU** (Start ∧ **EG** ! Heat)]

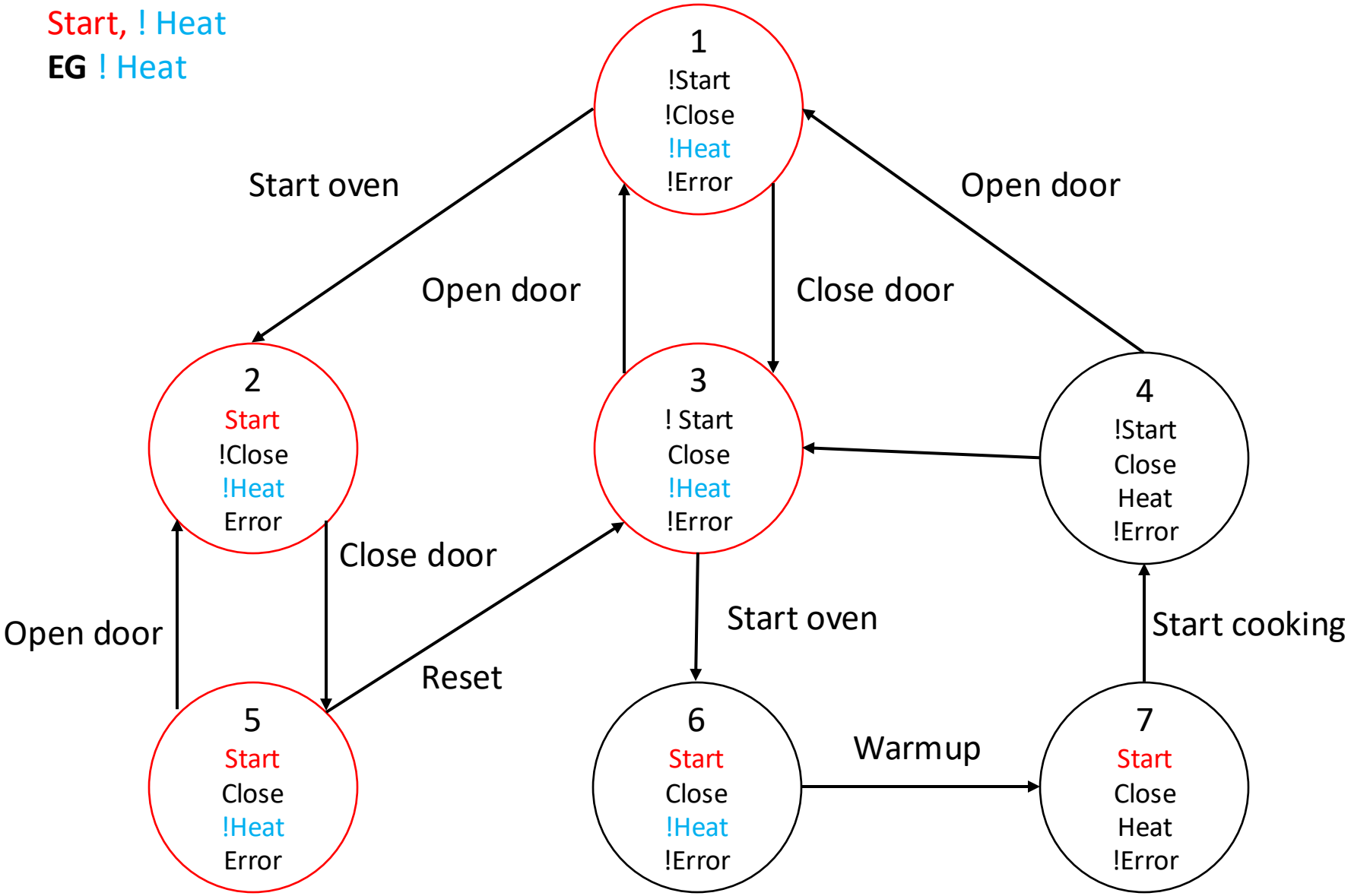


! EF (Start ∧ **EG** ! Heat)

Start, ! Heat

EG ! Heat

Nontrivial SCC of ! Heat



! EF (Start ∧ **EG** ! Heat)

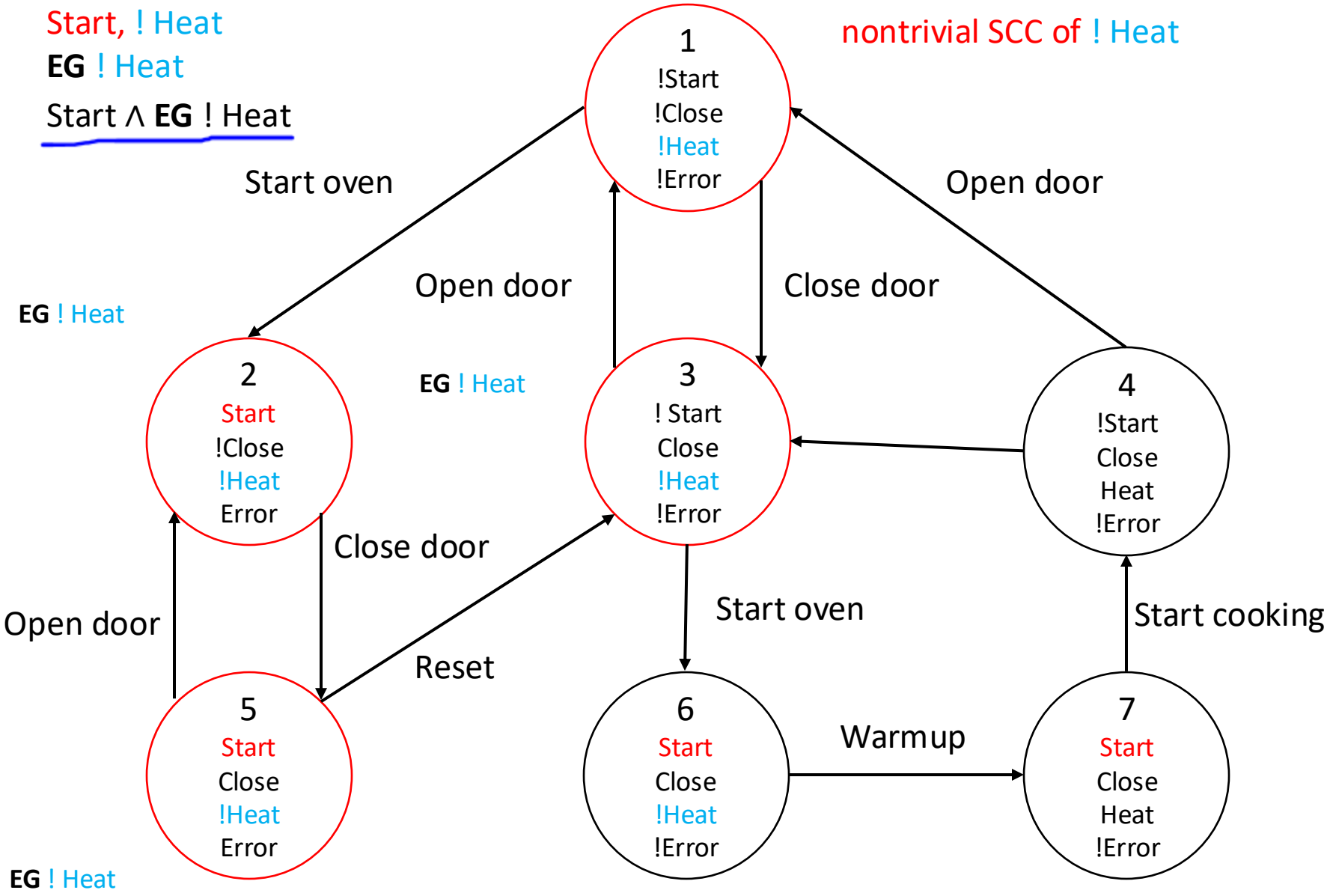
Start, ! Heat

EG ! Heat

Start ∧ **EG** ! Heat

EG ! Heat

Set of states that can reach n nontrivial SCC of ! Heat

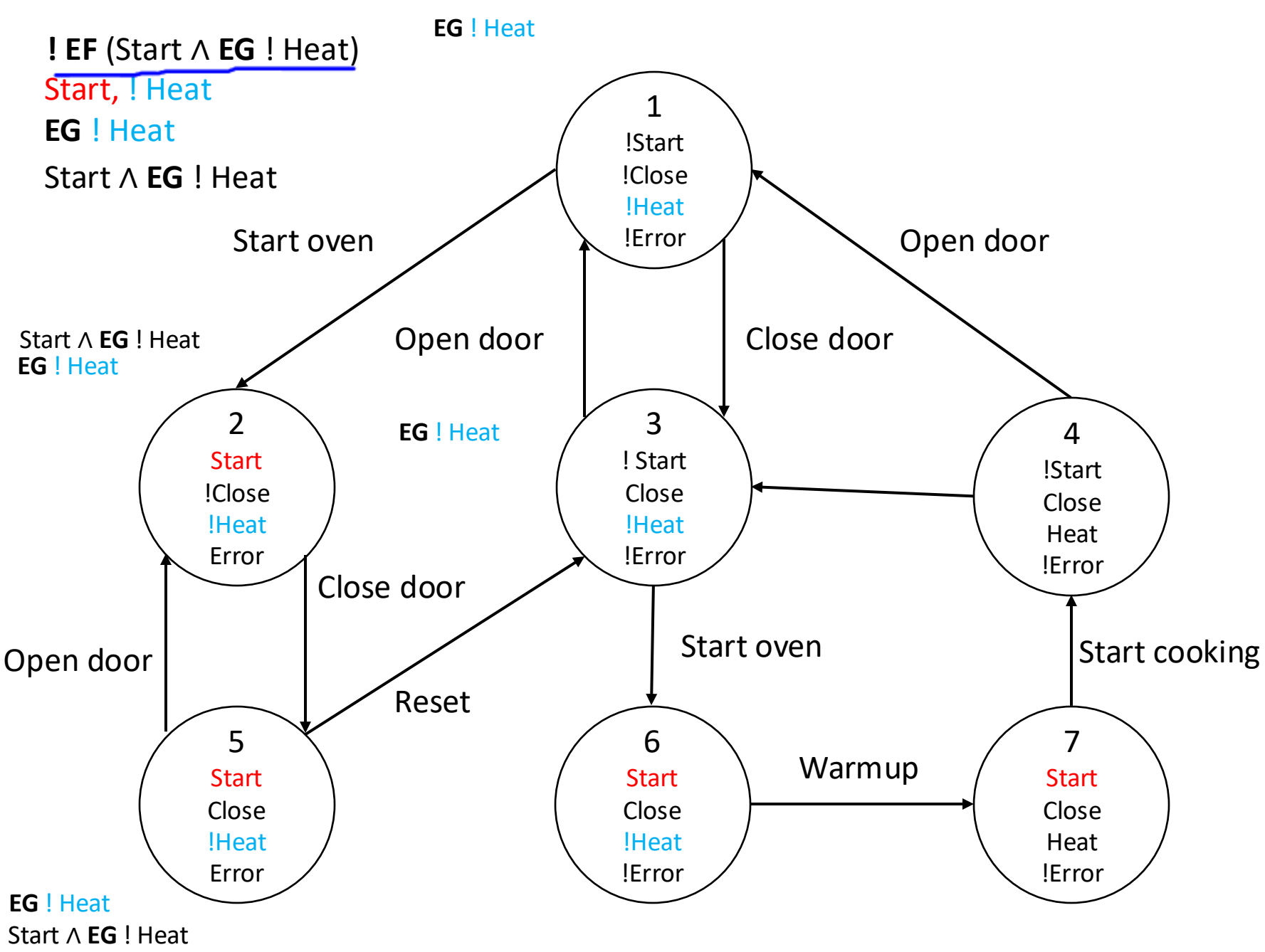


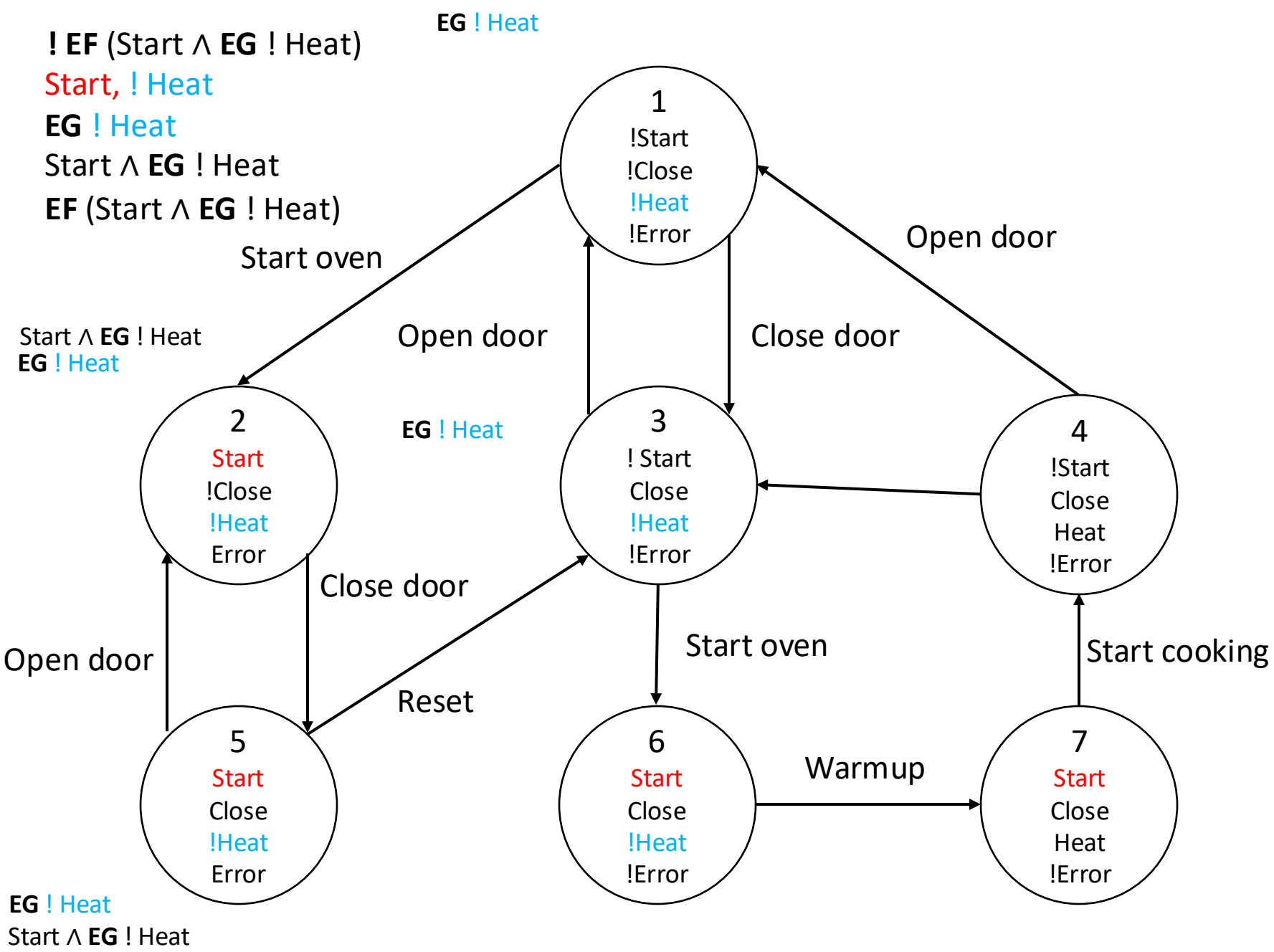
! EF (Start ∧ **EG** ! Heat)

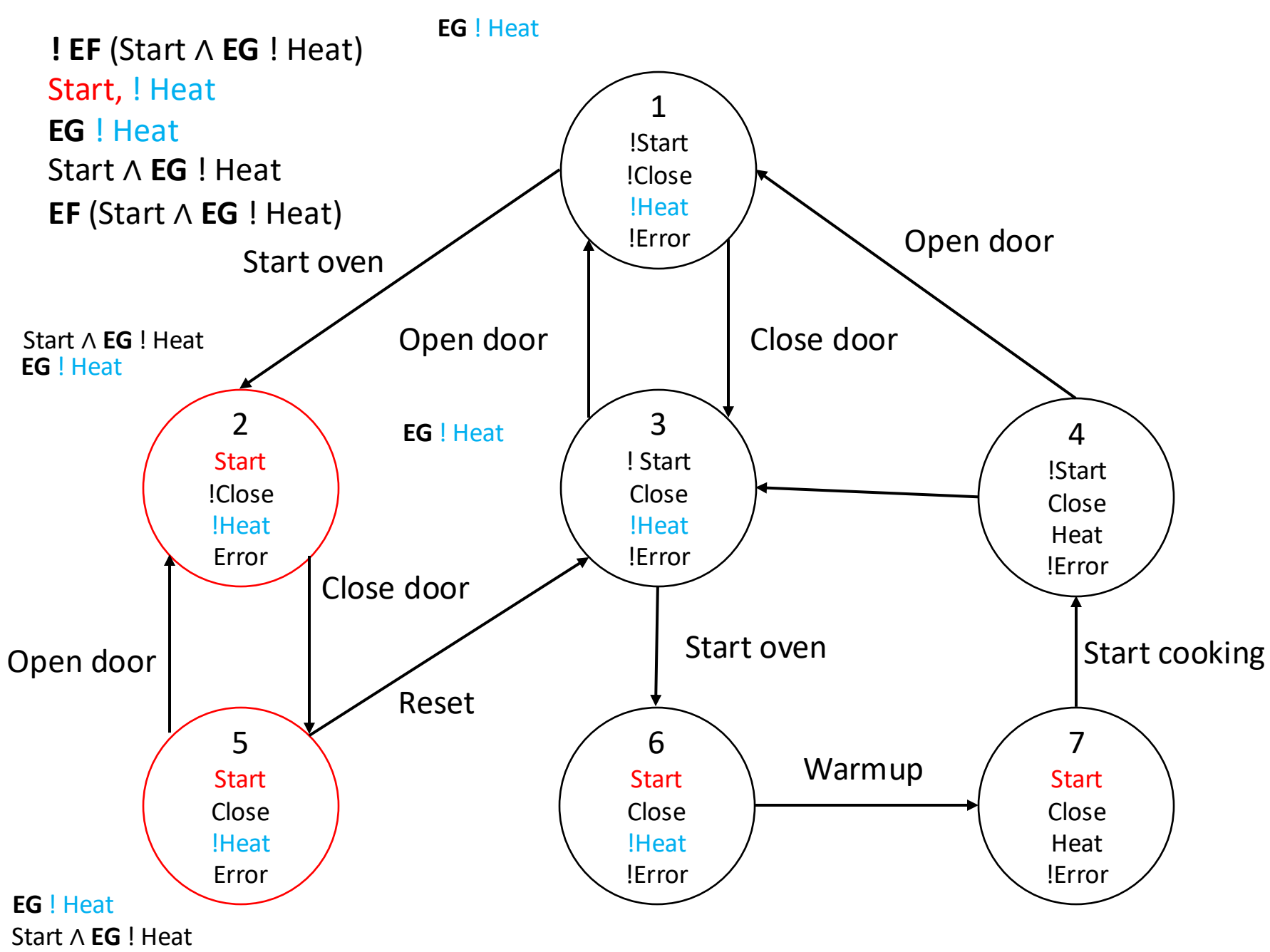
Start, ! Heat

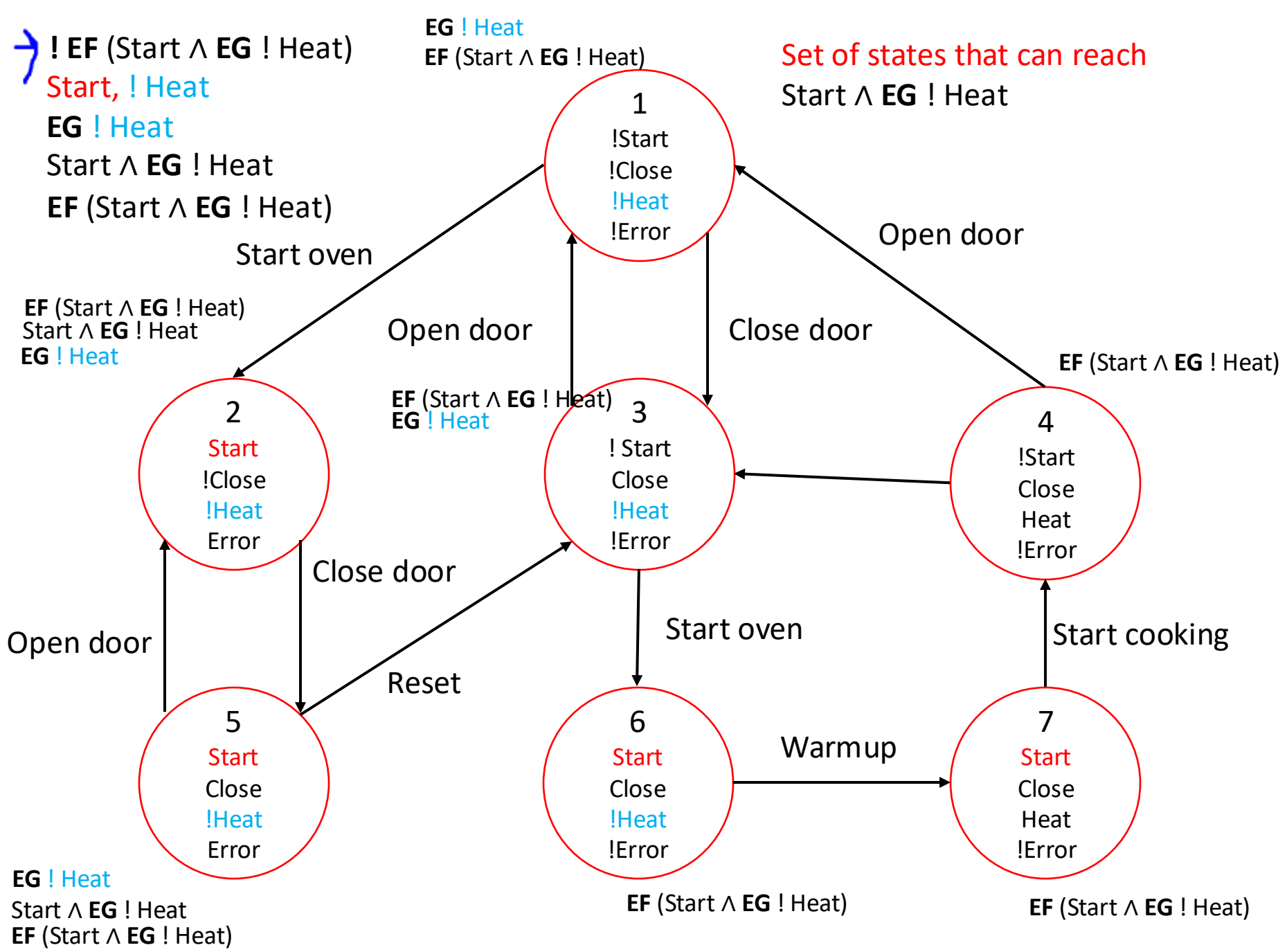
EG ! Heat

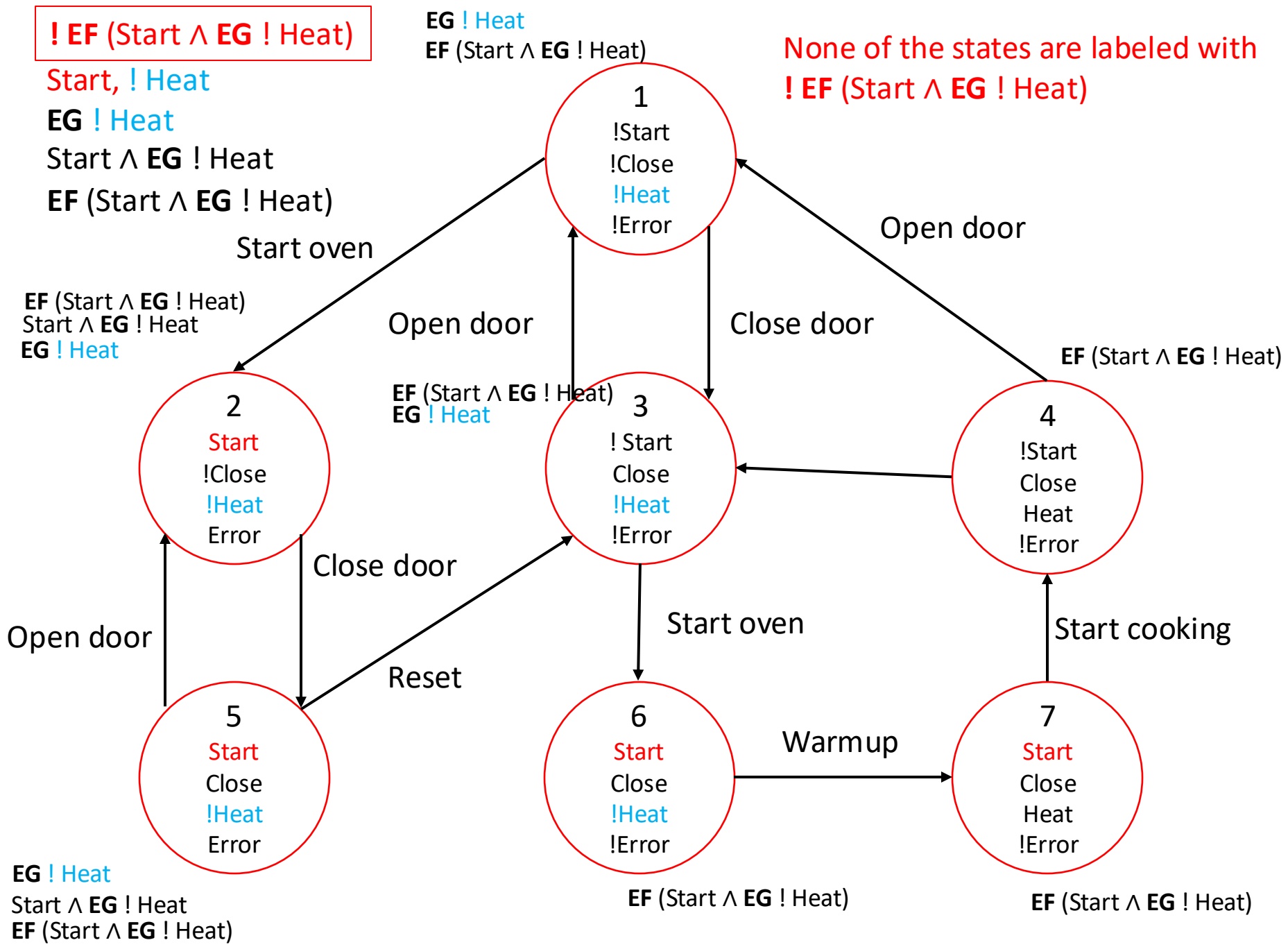
Start ∧ **EG** ! Heat




! EF (Start ∧ EG ! Heat)
Start, ! Heat
EG ! Heat
Start ∧ EG ! Heat
EF (Start ∧ EG ! Heat)
EG ! Heat
1
!Start
!Close
!Heat
!Error
Start oven
Open door
Close door
Open door
Start ∧ EG ! Heat
EG ! Heat
2
Start
!Close
!Heat
Error
EG ! Heat
3
! Start
Close
!Heat
!Error
4
!Start
Close
Heat
!Error
Close door
Open door
Start oven
Start cooking
Reset
5
Start
Close
!Heat
Error
6
Start
Close
!Heat
!Error
Warmup
7
Start
Close
Heat
!Error
EG ! Heat
Start ∧ EG ! Heat


! EF (Start ∧ EG ! Heat)
Start, ! Heat
EG ! Heat
Start ∧ EG ! Heat
EF (Start ∧ EG ! Heat)
EG ! Heat
1
!Start
!Close
!Heat
!Error
Start oven
Open door
Close door
Open door
Start ∧ EG ! Heat
EG ! Heat
2
Start
!Close
!Heat
Error
EG ! Heat
3
! Start
Close
!Heat
!Error
4
!Start
Close
Heat
!Error
Close door
Open door
Start oven
Start cooking
Reset
5
Start
Close
!Heat
Error
6
Start
Close
!Heat
!Error
Warmup
7
Start
Close
Heat
!Error
EG ! Heat
Start ∧ EG ! Heat


! EF (Start ∧ EG ! Heat)
Start, ! Heat
EG ! Heat
Start ∧ EG ! Heat
EF (Start ∧ EG ! Heat)
EG ! Heat
EF (Start ∧ EG ! Heat)
Set of states that can reach
Start ∧ EG ! Heat
1
!Start
!Close
!Heat
!Error
Start oven
Open door
Close door
Open door
EF (Start ∧ EG ! Heat)
Start ∧ EG ! Heat
EG ! Heat
EF (Start ∧ EG ! Heat)
EG ! Heat
2
Start
!Close
!Heat
Error
3
! Start
Close
!Heat
!Error
4
!Start
Close
Heat
!Error
Close door
Open door
Start oven
Start cooking
Reset
5
Start
Close
!Heat
Error
6
Start
Close
!Heat
!Error
7
Start
Close
Heat
!Error
Warmup
EG ! Heat
Start ∧ EG ! Heat
EF (Start ∧ EG ! Heat)
EF (Start ∧ EG ! Heat)
EF (Start ∧ EG ! Heat)


! EF (Start ∧ EG ! Heat)
Start, ! Heat
EG ! Heat
Start ∧ EG ! Heat
EF (Start ∧ EG ! Heat)
EG ! Heat
EF (Start ∧ EG ! Heat)
None of the states are labeled with
! EF (Start ∧ EG ! Heat)
1
!Start
!Close
!Heat
!Error
Start oven
Open door
Close door
Open door
EF (Start ∧ EG ! Heat)
Start ∧ EG ! Heat
EG ! Heat
EF (Start ∧ EG ! Heat)
EG ! Heat
2
Start
!Close
!Heat
Error
3
! Start
Close
!Heat
!Error
4
!Start
Close
Heat
!Error
Close door
Open door
Start oven
Start cooking
Reset
5
Start
Close
!Heat
Error
6
Start
Close
!Heat
!Error
7
Start
Close
Heat
!Error
Warmup
EG ! Heat
Start ∧ EG ! Heat
EF (Start ∧ EG ! Heat)
EF (Start ∧ EG ! Heat)
EF (Start ∧ EG ! Heat)

Summary

- CTL (Puneli) gives a formal language for describing automaton requirements allowing quantification (A, E) over executions and temporal operators (U, G, F)
  - Many other variations of temporal logics have been invented, e.g., Linear Temporal Logic, Signal Temporal Logic, Metric Temporal Logic
- Explicit state CTL model checking
  - Inductively label states that satisfy subformulas